

# **PMBT6428**

# 50 V, 100 mA NPN general purpose transistor

**Product data sheet** 

# 1. General description

NPN transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 50 V)
- AEC-Q101 qualified

# 3. Applications

- · General purpose switching and amplification
- · Telephony and professional communication equipment

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	50	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 0.1 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	250	-	650	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	□ 3	С
2	Е	emitter		J
3	С	collector		В—Қ
				L E
			SOT23	sym123



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# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package	age					
	Name	Description	Version				
PMBT6428	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23				

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PMBT6428	%1K

<sup>[1] % =</sup> placeholder for manufacturing site code

### 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
1110-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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### 10. Characteristics

#### **Table 7. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified

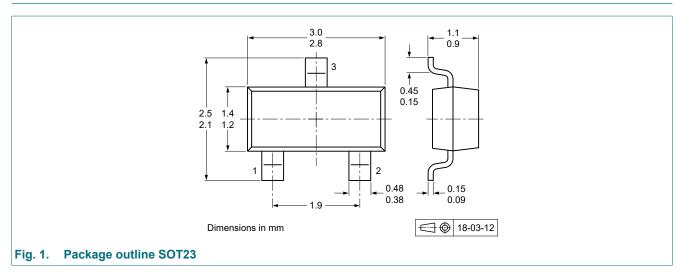
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}; T_j = 25 \text{ °C}$	-	-	10	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A	-	-	10	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 0.1 mA; T <sub>amb</sub> = 25 °C	250	-	650	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	250	-	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	250	-	-	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C	-	-	200	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; T <sub>amb</sub> = 25 °C	-	-	600	mV
$V_{BE}$	base-emitter voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	560	-	660	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	3	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = 0.5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	12	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	700	-	MHz

# 11. Test information

### **Quality information**

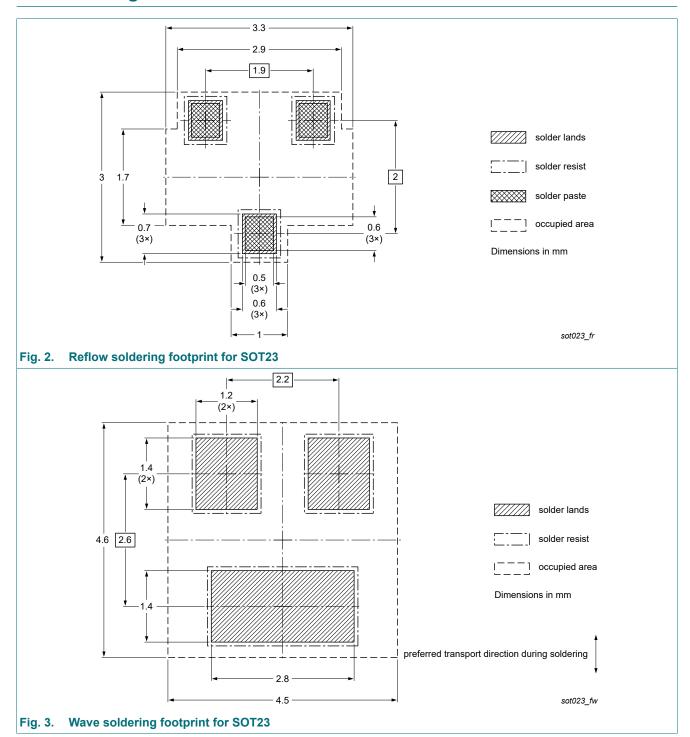
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



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# 13. Soldering



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# 14. Revision history

#### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMBT6428 v.3	20230726	Product data sheet	-	PMBT6428_PMBT6429 v.2			
Modifications:	guidelines of • Legal texts ha	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Family data sheet splitted to single type data sheets.</li> </ul>					
PMBT6428_PMBT6429 v.2	200401227	Product data sheet	-	PMBT6428_PMBT6429 v.1			
PMBT6428_PMBT6429 v.1	19990427	Product data	-	-			

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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